**Program:** B. Tech Data Science (Business Analytics)  
**Semester:** IV

<table>
<thead>
<tr>
<th>Course/Module: Advanced Database Management System</th>
<th>Module Code: BTDS04003</th>
</tr>
</thead>
</table>

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Evaluation Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lecture (Hours per week)</td>
<td>Practical (Hours per week)</td>
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<td>2</td>
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</tbody>
</table>

**Pre-requisite:** Database Management System (from SEM III)

**Objectives:**
- Expand the knowledge gained in Database Management Systems in several directions like Non-Relational data models, deductive (Intelligent) database systems, Distributed systems, web based systems and object oriented systems etc.

**Course Outcomes:**
After completion of the course, students would be able to:
- Design database using concept of extended entity relationship model.
- Implement functions and procedures using concepts of PL/SQL.
- Implement object oriented concepts in database.
- Compare and contrast different types of advance database management systems.
- Describe database Administration and its management.

**Detailed Syllabus:**

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
<th>Duration</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>The Extended Entity Relationship Model and Object Model: The ER model revisited, Motivation for complex data types, User defined abstract data types and structured types, subclasses, super classes, Inheritance, Specialization and Generalization, Constraints and characteristics of specialization and generalization</td>
<td>05</td>
</tr>
<tr>
<td>2.</td>
<td>Procedural Language/Structured Query language (PL/SQL): Introduction to PL/SQL, Disadvantages of SQL and advantages of PL/SQL, PL/SQL block structure, block data types, block variable declaration, exception handling, Cursors, types of cursors, functions, procedures, triggers.</td>
<td>07</td>
</tr>
<tr>
<td>3.</td>
<td>Object Oriented Databases: Overview of object oriented concepts, object identity, object structure and type constructions, Encapsulation of operations, Method and persistence, Type hierarchies and Inheritance, Type extents and queries, Complex objects; Database schema design for OODBMS; OQL, Persistent programming language; OODBMS architecture and storage issues; Transaction and Concurrency control, example of ODBMS.</td>
<td>05</td>
</tr>
<tr>
<td>4.</td>
<td>Object Relational and Extended Relational Databases: Database design for an ORDBMS, Nested relations and collections, storage and access methods,</td>
<td>04</td>
</tr>
</tbody>
</table>
### Query processing and optimization, An overview of SQL3, Implementation issues of extended type; System comparison of RDBMS, OODBMS, and ORDBMS.

**5. Parallel and Distributed Databases and Client-Server Architecture:**
- Architectures for parallel database, Parallel query evaluation, Parallelizing individual operations, Sorting, Joins, Distributed Database Concepts, Data Fragmentation, Replication, and allocation techniques for distributed database design; Query processing in distributed databases; Concurrency control and Recovery in distributed databases. An overview of Client-Server architecture.

**6. Databases on the web and Semi-Structured Data:**
- Web interfaces of the web. Overview of XML; data XML applications; The semi structured data model, Implementation issues, Indexes for data.

**7. Enhanced data models for Advanced applications:**
- Active database concepts, Temporal database concepts, Spatial database concepts and architecture, Deductive databases and Query processing, Mobile databases, Geographic information systems.

**8. Introduction to Data Warehousing & Mining:**
- Purpose of Data Warehousing, Introduction & conceptual structure of Data Warehouse, Multidimensional schemas, OLAP, Introduction to Data Mining, of Data Warehousing & Data Mining

Total 45

**Text Books:**

**Reference Books:**
7. Ivan Bayross, Oracle Developer 2000, BPB.

**Any other information:** NIL

**Details of Internal Continuous Assessment (ICA):**
- Test Marks: 20
- Term Work Marks: 30

**Details of Term work:**

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(Signature)
(Prepared by Concerned Faculty/HOD)

(Signature)
(Approved by Dean)
• Minimum 10 practical experiments covering all the topics.
• Minimum two Assignments.
• Two class tests.
Program: B. Tech. – Data Science (Business Analytics)  Semester :IV
Course/Module: Operations Research  Module Code: BTDS04004

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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</tr>
</thead>
<tbody>
<tr>
<td>Lecture (Hours per week)</td>
<td>Practical (Hours per week)</td>
</tr>
<tr>
<td>3</td>
<td>0</td>
</tr>
</tbody>
</table>

Pre-requisite: Course on Linear algebra and Matrix Algebra

COURSE OBJECTIVES:
- Introduce optimization methodology as a valuable decision support tool.
- Help develop skills in building and solving optimization models for variety of engineering and Data Science related decision problems.
- Expose key mathematical concepts underlying various optimization models and algorithms.
- Provide hands-on experience with optimization software for solving and analyzing optimization models.

Outcomes:
After completion of the course, students would be able to:
- Formulate deterministic mathematical programs in various practical systems
- Understand basic optimization techniques
- Be able to interpret the results of a model and present the insights (sensitivity, duality)
- Know the limitations of different solution methodology
- Use software to solve problems

Detailed Syllabus:

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Linear Programming Problem (LPP) : Mathematical Model, Standard Form, Canonical Form, Cost Minimization &amp; Profit Maximization Models, Graphical Polygon Corners Method, Graphical Iso-Profit/Iso-Cost Line Method, Analytical /Trial &amp; Error Method, Simplex Method, Big-M Simplex Method, Two-Phase Simplex Method, Primal Vs Dual, Formulation of Dual from Primal, Comparison of Solutions of Primal &amp; Dual, Economic Interpretation of Dual Variables, Shadow Pricing, Sensitivity Analysis</td>
</tr>
</tbody>
</table>

Signature  
(Prepared by Concerned Faculty/HOD)

Signature  
(Approved by Dean)
### Course Content

<table>
<thead>
<tr>
<th>Module</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>Assignment Problem (AP) : Mathematical Model, Cost Minimization &amp; Profit Maximization Models of Square Matrix &amp; Non-Square Matrix Problems, Hungarian/Reduced Matrix/Flood’s Techniques Method, Optimality Test Criteria for OFS.</td>
</tr>
<tr>
<td>4</td>
<td>Decision Making under Conditions of Certainty, Uncertainty &amp; Risk : Maximax, MaxiMin, MiniMax, Hurwicz, Laplace Criteria &amp; Methods, EMV, EOL &amp; EVPI Calculations, Incremental/Marginal Analysis Method.</td>
</tr>
</tbody>
</table>

**Total:** 45

### Text Books:

### Reference Books:

### Any other information: NIL

### Details of Internal Continuous Assessment (ICA):
- **Test Marks:** 20
- **Term Work Marks:** 30

**Details of Term work:**
- Minimum Two class tests.
- Minimum two assignments
Program: B. Tech. – Data Science (Business Analytics)  | Semester: IV

Course/Module: Cyber Security  | Module Code: BTDS04006

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
<th>Evaluation Scheme</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lecture (Hours per week)</td>
<td>Practical (Hours per week)</td>
</tr>
<tr>
<td>3</td>
<td>2</td>
</tr>
</tbody>
</table>

Marks Scaled to 50  | Marks Scaled to 50

Pre-requisite: Basic syntax and semantics of programming languages, object-oriented programming

Objectives:
After completion of the course, students would be able to:
- To give an introduction to the field of cyber security. This course presents a balance of the managerial and the technical aspects of the discipline and addresses knowledge areas as specified in the CISSP (Certified Information Systems Security Professionals) certification.
- Learn about the threats to information and ways to overcome it

Outcomes:
Upon completion of this course students will be:
- Analyse threats to cyber security and understand various countermeasures.
- Describe various security technologies and policies
- Compare and contrast symmetric and asymmetric key cryptography.
- Understand various access control models
- Understand Risk Management and Business Continuity Planning
- Understand various legal and ethical issues related to cybercrime and computer forensics

Detailed Syllabus:

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
<th>Duration</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td><strong>Introduction:</strong> Basic Components of Computer security (CIA), characteristics of information vulnerabilities, threats, attacks and controls, goals of security, NSTISSC security model, security system development life cycle, computer criminals, internet standards and RFC.</td>
<td>05</td>
</tr>
<tr>
<td>2</td>
<td><strong>Design Principles:</strong> Various security attacks, method of defense, design principles, security policies, and types of security policies.</td>
<td>04</td>
</tr>
<tr>
<td>3</td>
<td><strong>Cryptography:</strong> Cryptography basics, transposition ciphers, substitution ciphers, AES, public key cryptography, stream and black ciphers, key management, digital signature</td>
<td>07</td>
</tr>
<tr>
<td>4</td>
<td><strong>Program Security</strong>: Secure programs, non-malicious program errors, viruses and other malicious code, types of viruses, attack mechanism of viruses, targeted malicious code, control agent program threats</td>
<td>04</td>
</tr>
<tr>
<td>---</td>
<td>---</td>
<td>---</td>
</tr>
<tr>
<td>5</td>
<td><strong>Authentication</strong>: Authentication basics, password, challenge response, SSO, two factor authentication, biometrics</td>
<td>02</td>
</tr>
<tr>
<td>6</td>
<td><strong>Access Control</strong>: Access control principles, ACL, DAC, MAC and role based access control, access control models, Kerberos</td>
<td>04</td>
</tr>
<tr>
<td>7</td>
<td><strong>Firewall</strong>: Kinds of firewalls, filtering, services, DMZ, implementing policies (default allow, default deny) on proxy, IDS, types of IDS, virtual private network, SSH</td>
<td>05</td>
</tr>
<tr>
<td>8</td>
<td><strong>Physical and Environmental Security</strong>: Introduction, physical security threats and measure</td>
<td>02</td>
</tr>
<tr>
<td>9</td>
<td><strong>Risk Management and Business Continuity planning</strong>: Risk analysis, various terminologies associated with risk management, risk assessment techniques, managing risks, steps for risk management, Business impact analysis, various terminologies associated with BIA, different types of continuity planning, testing and revising the plan.</td>
<td>04</td>
</tr>
<tr>
<td>10</td>
<td><strong>Laws, investigations and Ethics</strong>: Introduction, types of computer crimes, modus operandi, computer forensics, ethical issues in computer security.</td>
<td>03</td>
</tr>
<tr>
<td>11</td>
<td><strong>Contemporary issues related to Information Security</strong></td>
<td>05</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td><strong>45</strong></td>
<td><strong>Total</strong></td>
</tr>
</tbody>
</table>

**Text Books:**

**Reference Books:**

**Any other information**: NIL

**Details of Internal Continuous Assessment (ICA):**
- **Test Marks**: 20
- **Term Work Marks**: 30

**Details of Term work:**
- Practical based on few Experiments
SVKM’s NMIMS
Mukesh Patel School of Technology Management & Engineering

- Two class tests.
- Minimum two assignments
Program: B. Tech. – Data Science (Business Analytics)  
Semester: IV

Course/Module: Operating Systems  
Module Code: BTDS04007

<table>
<thead>
<tr>
<th>Teaching Scheme</th>
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<tbody>
<tr>
<td>Lecture (Hours per week)</td>
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<td>2</td>
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</tbody>
</table>

Pre-requisite: Computer Programming, Data Structure and Algorithm, Computer Organization and Architecture

Objectives:

- Design techniques, process management, processor scheduling; deadlocks, memory management, secondary memory management, file management; I/O systems, Unix systems.
- Knowledge of process control, threads, concurrency, memory management scheduling, I/O and files, distributed systems, security, networking will be enhanced

Outcomes:

After completion of the course, students would be able to:
1. Design functions, structures and history of operating systems
2. Understand design issues associated with operating systems
3. Design various process management concepts including scheduling, synchronization, deadlocks
4. Be familiar with multithreading

Detailed Syllabus:

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
<th>Duration</th>
</tr>
</thead>
</table>
| 1    | Operating Systems Overview:  
Operating systems objectives and functions, evolution of operating systems, characteristics of modern operating systems, basic concepts of processes, files system, system calls, shell, layered structure v/s monolithic structure of OS, introduction to distributed OS, RTOS, mobile OS | 05 |
| 2    | Process and Process Scheduling:  
Process description, process control block, threads, thread management, comparison between processes and threads, process scheduling: types, study and comparison of various scheduling algorithms | 09 |
| 3    | Process Concurrency:  
Principle of concurrency: mutual exclusion – hardware approaches, mutual exclusion – software approaches, semaphore, monitors, message parsing, reader / writer problem. | 09 |
| 4    | Deadlock:  
Principle of deadlock, deadlock prevention, deadlock avoidance, Ostrich algorithm, Banker’s algorithm, deadlock detection, dining philosophers’ problem. | 06 |
### Memory Management:
Memory management requirements, memory partitioning, virtual memory management, paging, segmentation, segmented paging, design and implementation issues in lagging and segmentation.

### I/O Management and Disc Scheduling:
I/O devices, organization of the I/O function, operating systems design issues, I/O buffering, disk scheduling and disk schedule algorithms, RAID, disk cache.

### File Management:
Overview, file organization, file directories, file sharing, record blocking, secondary storage management, UNIX file system.

### Case Study:
Mobile Operating Systems: introduction, process & thread management, memory management, file system and I/O management, comparison with Windows and Linux operating systems functions.

### Text Books:

### Reference Books:

### Any other information: NIL

### Total Marks of Internal Continuous Assessment (ICA) : 50 Marks

<table>
<thead>
<tr>
<th>Description of ICA</th>
<th>Marks</th>
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</thead>
<tbody>
<tr>
<td>Test Marks</td>
<td>20</td>
</tr>
<tr>
<td>Term Work Marks</td>
<td>30</td>
</tr>
<tr>
<td><strong>Total Marks</strong></td>
<td><strong>50</strong></td>
</tr>
</tbody>
</table>

### Details of Term work:
1. Minimum: Practical based on 10 Experiments
2. Minimum Two class tests.
3. Minimum two assignments/mini project/presentation/Quiz
### Program: B. Tech. – Data Science (Business Analytics)  
**Semester : IV**

### Course/Module: Internet of Things  
**Module Code : BTDS04008**

<table>
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<tr>
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<td>3</td>
<td>2</td>
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</table>

**Pre-requisite:** C, C++, Basics of computer network

**Objectives:**
- To understand fundamental of IOT architecture and protocols

**Outcomes:**
After completion of the course, students would be able to:
- Introduction to IOT architecture and M2M technology
- Understanding the IOT protocols
- Identify and analyse the various cloud components for IOT
- Understanding the security issues in IOT

**Detailed Syllabus:**

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
<th>Duration</th>
</tr>
</thead>
</table>
| 1    | **Introduction to IOT**  
IoT-An Architectural Overview– Building an architecture, Main design principles and needed capabilities, An IoT architecture outline, standards considerations. M2M and IoT Technology Fundamentals | 06 |
| 2    | **IOT Fundamentals**  
IoT reference Model - IoT Reference Architecture Introduction, Functional View, Information View, Deployment and Operational View, Other Relevant architectural views, Devices and gateways, Local and wide area networking, Data management, Business processes in IoT, Everything as a Service (XaaS), M2M and IoT Analytics, Knowledge Management | 08 |
| 3    | **IOT protocols**  

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**Signature**  
(Prepared by Concerned Faculty/HOD)  

**Signature**  
(Approved by Dean)
**IOT and Cloud**

**IOT Security**

**Case study**
Smart Cities, Smart Home Industrial Control, Smart Social Networks, Big Data Analytics

<table>
<thead>
<tr>
<th>4</th>
<th>IOT and Cloud</th>
<th>09</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>IOT Security</td>
<td>09</td>
</tr>
<tr>
<td>6</td>
<td>Case study</td>
<td>05</td>
</tr>
<tr>
<td>Total</td>
<td></td>
<td>45</td>
</tr>
</tbody>
</table>

**Text Books:**

**Reference Books:**
3. Peter Waher, —Learning Internet of Things, PACKT publishing, BIRMINGHAM – MUMBAI
4. Practical Internet of Things Security (Kindle Edition) by Brian Russell, Drew Van Duren

**Any other information:** NIL

**Details of Internal Continuous Assessment (ICA):**
Test Marks: 20
Term Work Marks: 30

**Details of Term work:**
- Minimum Two class tests.
- Minimum two assignments
Program: B. Tech. – Data Science (Business Analytics)

Course/Module: Statistical Method - I

Module Code: BTDS04009

Teaching Scheme

<table>
<thead>
<tr>
<th>Lecture (Hours per week)</th>
<th>Practical (Hours per week)</th>
<th>Tutorial (Hours per week)</th>
<th>Credit</th>
<th>Internal Continuous Assessment (ICA) (Marks -50)</th>
<th>Term End Examinations (TEE) (Marks -100 in Question Paper)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>2</td>
<td>0</td>
<td>4</td>
<td>Marks Scaled to 50</td>
<td>Marks Scaled to 50</td>
</tr>
</tbody>
</table>

Pre-requisite: Managing Uncertainty (SEM - III)

Objectives:
- To provide advanced statistical background for analysing data and drawing inferences from that analysis
- Predicative Analytics using liner and generalized liner model

Outcomes:
After completion of the course, students would be able to:
- Students will be able to learn advanced statistical technique and apply them to the analysis of real data sets from different fields.

Detailed Syllabus:

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
<th>Duration</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>ANOVA/MANOVA: Chi-Square as a test of independent, Chi-square as a Test of goodness of fit: Testing the Appropriateness of a Distribution, Analysis of Variance, Multivariate analysis of variance</td>
<td>3</td>
</tr>
<tr>
<td>2</td>
<td>Regression Model:</td>
<td></td>
</tr>
<tr>
<td></td>
<td>a) Least squares and linear regression: Introduction; Notation; Ordinary least squares; Regression to the mean; Linear regression; Residuals; Regression inference</td>
<td></td>
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<td></td>
<td>b) Multivariable regression: Multivariate regression; Multivariate examples; Adjustment; Residual variation and diagnostics; Multiple variables , Interaction Terms, Non-linear Transformations of the Predictors, Qualitative Predictors</td>
<td>3</td>
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<tr>
<td></td>
<td>c) Multiple Regression Analysis: The Problem of Estimation and the Problem of Inference</td>
<td>3</td>
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<tr>
<td></td>
<td>d) Dummy Variable Regression Models</td>
<td>3</td>
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<tr>
<td></td>
<td>e) Multi-collinearity. Heteroscedasticity, Autocorrelation</td>
<td>4</td>
</tr>
<tr>
<td></td>
<td>f) Econometric Modelling: Model Specification and Diagnostic Testing</td>
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<td></td>
<td>g) Correlation and Covariance Analysis</td>
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<td></td>
<td>h) Canonical Analysis, Canonical Roots/v variates</td>
<td></td>
</tr>
</tbody>
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(Prepared by Concerned Faculty/HOD)

Signature
(Approved by Dean)
| Extension of regression analysis: | 5 |
| Ridge Regression, The Lasso | |
| Nonlinear Regression Models: Approaches to Estimating Nonlinear Regression models | 5 |
| Generalized linear models: | 3 |
| Logistic Regression, Binary outcomes, Count outcomes, Multiple Logistic Regression | 3 |
| **Total** | **45** |

**Text Books:**

**Reference Books:**

**Any other information:** NIL

**Details of Internal Continuous Assessment (ICA):**
- Test Marks: 20
- Term Work Marks: 30

**Details of Term work:**
- Practical based on 10 Experiments
- Two class tests.
- Minimum two assignments
Program: B. Tech Data Science (Business Analytics)  
Semester: IV

Course/Module: Computer Organization and Architecture  
Module Code: BTDS04010

<table>
<thead>
<tr>
<th>Lecture (Hours per week)</th>
<th>Practical (Hours per week)</th>
<th>Tutorial (Hours per week)</th>
<th>Credit</th>
<th>Internal Continuous Assessment (ICA) (Marks -50)</th>
<th>Term End Examinations (TEE) (Marks -100 in Question Paper)</th>
</tr>
</thead>
<tbody>
<tr>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>Marks Scaled to 50</td>
<td>Marks Scaled to 50</td>
</tr>
</tbody>
</table>

Objectives:
- To provide knowledge of logic circuits theory, elementary analysis and its implementation in practical cases, the popular logic families and their characteristics that will help to understand design of complex digital circuits and systems.
- To have a thorough understanding of the basic structure and operation of a digital computer.
- To discuss in detail the operation of ALU including algorithms and implementation of fixed point and floating point addition, subtraction, multiplication and division.
- To study different ways of communicating with I/O devices and standard I/O interfaces.
- To study hierarchical memory system including cache memories and virtual memory.

Outcomes:
After completion of the course, students would be able to:
- Solve basic binary math operations using the logic gates.
- Demonstrate programming proficiency using various logical elements to design simple logical units.
- Design different units that are elements to typical computer's CPU.
- Understand fundamental concepts of Computer organization and architecture including BUS and MEMORY.
- Understand operation of CPU, Control Unit, I/O and parallel processing.

Detailed Syllabus: (per session plan)

<table>
<thead>
<tr>
<th>Unit</th>
<th>Description</th>
<th>Duration</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td><strong>Number Systems:</strong> Decimal, Binary, Octal and Hexadecimal number system and conversion, Binary weighted codes, signed number binary order 1's and 2's complement codes, binary arithmetic.</td>
<td>3</td>
</tr>
<tr>
<td>Chapter</td>
<td>Topic Description</td>
<td></td>
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<tr>
<td>2</td>
<td><strong>Boolean Algebra:</strong> Binary logic function, Boolean laws, Truth tables, Associative and distributive properties, Demorgan’s theorems, Realization of switching function using logic gates.</td>
<td>5</td>
</tr>
<tr>
<td>3</td>
<td><strong>Combinational Logic and its analysis and design:</strong> Intro. Combinational circuit, code conversion, decoder encoder priority encoder, multiplexers as function generators, Binary address, subtractor, BCD adder, binary comparator and ALU.</td>
<td>5</td>
</tr>
<tr>
<td>4</td>
<td><strong>Sequential Logic:</strong> Sequential circuits, flip-flop, clock and edge triggered flip-flop timing specifications counter asynchronous and synchronous, counter design with state equation registers, serial in serial out shift registers, tri-state register, register transfer timing consideration. State diagram and tables, transition table, excitation table and equation. Example using flip-flop. Construction of state diagram and counter design.</td>
<td>3</td>
</tr>
<tr>
<td>5</td>
<td><strong>Overview:</strong> General organization and architecture, structural / functional view of a computer, Brief history of computer.</td>
<td>3</td>
</tr>
<tr>
<td>6</td>
<td><strong>System Buses:</strong> Computer functions and flow control, interrupts and interconnection, BUS design and timing, Hierarchy and arbitration.</td>
<td>3</td>
</tr>
<tr>
<td>7</td>
<td><strong>Memory Organization:</strong> Internal memory: characteristics, Hierarchy, semiconductor main memory: Types of RAM, Chip Logic, Memory module organization, Cache memory: element of cache design, address mapping and translation, replacement algorithm: Advanced DRAM org; performance characteristics of two – level memories, external memory: Magnetic disk, Tape, Raid, Optical memory, High Speed memories; associative memory, interleaved memory.</td>
<td>5</td>
</tr>
<tr>
<td>8</td>
<td><strong>CPU:</strong> Basic instruction cycle, instruction sets, formats and addressing, processor organization, register organization, instruction pipelining, co-processors, pipeline processors, RISC computer, RISC vrs CISC.</td>
<td>5</td>
</tr>
<tr>
<td>9</td>
<td><strong>Control Unit:</strong> micro – operations, hardwired implementation, micro programming control, Micro – instruction format, application of microprogramming.</td>
<td>5</td>
</tr>
<tr>
<td>10</td>
<td><strong>Input and Output Unit:</strong> External device, keyboard, monitor, disk drive and device and device driver, I/O modules; programmed I/O,</td>
<td>4</td>
</tr>
</tbody>
</table>
interrupt driven I/O, DMA, I/O channels and I/O processors, serial transmission and synchronization.

<table>
<thead>
<tr>
<th>11</th>
<th>Multiprocessor processor Organization: Flynn’s classification of parallel processing systems, pipelining concepts</th>
<th>4</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td><strong>Total</strong></td>
<td>45</td>
</tr>
</tbody>
</table>

Text Book:


Reference Books:


Any other information: NIL

Details of Internal Continuous Assessment (ICA):

Test Marks: 20
Term Work Marks: 30

Details of Term work:

- Two tests
- Assignments
- Quiz/presentations/tutorials/viva